

EXHIBIT

12

Table of Contents

**UNITED STATES
SECURITIES AND EXCHANGE COMMISSION**

Washington, D.C. 20549

FORM 10-K

(Mark One)

- ☒ **ANNUAL REPORT PURSUANT TO SECTION 13 OR 15(d) OF THE SECURITIES
EXCHANGE ACT OF 1934.**

For the fiscal year ended December 29, 2002

OR

- ☐ **TRANSITION REPORT PURSUANT TO SECTION 13 OR 15(d) OF THE SECURITIES
EXCHANGE ACT OF 1934.**

For the transition period from _____ to _____

Commission File Number 1-7882

ADVANCED MICRO DEVICES, INC.

(Exact name of registrant as specified in its charter)

Delaware

(State or other jurisdiction of incorporation or organization)

94-1692300

(I.R.S. Employer Identification No.)

One AMD Place, Sunnyvale, California
(Address of principal executive offices)

94086
(Zip Code)

(408) 732-2400

(Registrant's telephone number, including area code)

Securities registered pursuant to Section 12(b) of the Act:

(Title of each class)

\$ 01 Par Value Common Stock

(Name of each exchange
on which registered)

New York Stock Exchange

Securities registered pursuant to Section 12(g) of the Act:

None

Indicate by check mark whether the registrant (1) has filed all reports required to be filed by Section 13 or 15(d) of the Securities Exchange Act of 1934 during the preceding 12 months (or for such shorter period that the registrant was required to file such reports), and (2) has been subject to such filing requirements for the past 90 days. Yes ☒ No ☐

Table of Contents

Advanced Micro Devices, Inc.
FORM 10-K
For The Fiscal Year Ended December 29, 2002

INDEX

<u>PART I</u>	1
ITEM 1. <u>BUSINESS</u>	1
ITEM 2. <u>PROPERTIES</u>	11
ITEM 3. <u>LEGAL PROCEEDINGS</u>	12
ITEM 4. <u>SUBMISSION OF MATTERS TO A VOTE OF SECURITY HOLDERS</u>	12
<u>PART II</u>	13
ITEM 5. <u>MARKET FOR REGISTRANT'S COMMON EQUITY AND RELATED STOCKHOLDER MATTERS</u>	13
ITEM 6. <u>SELECTED FINANCIAL DATA</u>	14
ITEM 7. <u>MANAGEMENT'S DISCUSSION AND ANALYSIS OF FINANCIAL CONDITION AND RESULTS OF OPERATIONS</u>	15
ITEM 7A. <u>QUANTITATIVE AND QUALITATIVE DISCLOSURE ABOUT MARKET RISK</u>	41
ITEM 8. <u>FINANCIAL STATEMENTS AND SUPPLEMENTARY DATA</u>	44
ITEM 9. <u>CHANGES IN AND DISAGREEMENTS WITH ACCOUNTANTS ON ACCOUNTING AND FINANCIAL DISCLOSURE</u>	78
<u>PART III</u>	79
ITEM 10. <u>DIRECTORS AND EXECUTIVE OFFICERS OF THE REGISTRANT</u>	79
ITEM 11. <u>EXECUTIVE COMPENSATION</u>	79
ITEM 12. <u>SECURITY OWNERSHIP OF CERTAIN BENEFICIAL OWNERS AND MANAGEMENT AND RELATED STOCKHOLDER MATTERS</u>	79
ITEM 13. <u>CERTAIN RELATIONSHIPS AND RELATED TRANSACTIONS</u>	79
ITEM 14. <u>CONTROLS AND PROCEDURES</u>	79
<u>PART IV</u>	80
ITEM 15. <u>EXHIBITS, FINANCIAL STATEMENT SCHEDULES, AND REPORTS ON FORM 8-K</u>	80
<u>SIGNATURES</u>	F-1
<u>CERTIFICATION</u>	F-2

Table of Contents

processors in April 2003 and our AMD Athlon 64 processors in September 2003. These processors are designed to provide high performance for both 32-bit and 64-bit applications in servers and in desktop and mobile PCs. The success of these processors is subject to risks and uncertainties including our ability to produce them in a timely manner on new process technologies, including silicon-on-insulator technology, in the volume and with the performance and feature set required by customers; their market acceptance; the availability, performance and feature set of motherboards and chipsets designed for our eighth-generation processors; and the support of the operating system and application program providers for our 64-bit instruction set.

If we were to lose Microsoft Corporation's support for our products, our ability to market our processors would be materially adversely affected. Our ability to innovate beyond the x86 instruction set controlled by Intel depends on support from Microsoft in its operating systems. If Microsoft does not provide support in its operating systems for our x86 instruction sets, including our x86-64 technology that will be introduced with our AMD Athlon 64 and AMD Opteron processors, independent software providers may forego designing their software applications to take advantage of our innovations. If we fail to retain the support and certification of Microsoft, our ability to market our processors could be materially adversely affected.

The completion and impact of our restructuring program and cost reductions could adversely affect us. On November 7, 2002, we announced that we were formulating the 2002 Restructuring Plan to address the continuing industry-wide weakness in the semiconductor industry by adjusting our cost structure to industry conditions. Pursuant to the 2002 Restructuring Plan, we intend to reduce our fixed costs as a percentage of total costs over time from approximately 80 percent to approximately 70 percent. We also expect to reduce our expenses by approximately \$100 million per quarter by the second quarter of 2003. As a result, we expect total expenses in 2003 to be reduced by approximately \$350 million based on current product demand forecasts. We cannot, however, be sure that the goals of the 2002 Restructuring Plan will be realized. The ultimate effects of the 2002 Restructuring Plan could prove to be adverse.

Weak market demand for our Flash memory products, the loss of a significant customer in the high-end mobile telephone market, or any difficulty in our transition to Mirrorbit technology may have a material adverse effect on us. The demand for Flash memory devices has been weak due to the sustained downturn in the communications and networking equipment industries and excess inventories held by our customers. In the third and fourth quarters of this year, our Flash memory product sales grew almost entirely based on strength in the high-end mobile phone market. Our sales in that market are concentrated in a few customers. In addition, we expect competition in the market for Flash memory devices to continue to increase as competing manufacturers introduce new products and industry-wide production capacity increases. We may be unable to maintain or increase our market share in Flash memory devices as the market develops and Intel and other competitors introduce new competing products. A decline in unit sales of our Flash memory devices, lower average selling prices, or a loss of a significant customer in the high-end mobile phone market, would have a material adverse effect on us.

In July 2002, we commenced production shipments of our first product with MirrorBit technology. Our MirrorBit technology is a new memory cell architecture that enables Flash memory products to hold twice as much data as standard Flash memory devices. A lack of customer acceptance, any substantial difficulty in transitioning our Flash memory products to MirrorBit technology or any future process technology could reduce our ability to be competitive in the market and could have a material adverse effect on us.

We rely on our joint venture with Fujitsu, FASL, and if that joint venture is terminated or amended significantly, we could be materially adversely affected. We continue to rely on our joint venture with Fujitsu, FASL, to manufacture our Flash memory devices. In addition, beginning in 2002, Fab 25 began operating as a foundry to FASL and as of year end, Fab 25 was devoted entirely to Flash memory device production for FASL. While the FASL joint venture has been successful to date, there can be no assurance that we and Fujitsu will elect to continue the joint venture in its current form, in some other form or at all, which could have a material adverse effect on us.